

## **SDG MODEL 12060**

### **20GB/S PROGRAMMABLE PATTERN GENERATOR**

PRODUCT SPECIFICATION

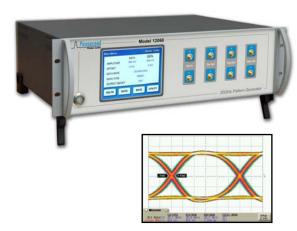
#### Features:

- Programmable data rate, amplitude, offset, and crossing point
- Differential data, pattern trigger, clock/n, and full rate clock outputs
- Integrated low jitter programmable clock source
- PRBS and user defined patterns
- Single bit error injection for testing error measurement setups
- Front panel touch screen GUI or USB computer control

## **Output Performance:**

- 25ps typical 10% to 90% rise and fall times
- 250mV to 2.0V output amplitude
- -2.0V to 3.0V offset window
- 35% to 65% programmable crossing point

# PatternPro<sup>™</sup> Line of Serial Data Test Instruments



## Applications:

- High-speed serial data testing to 20Gb/s
- 14Gb/s Fibre Channel Testing
- Semiconductor device and component testing
- R&D design verification

The Picosecond Pulse Labs' **Model 12060** is a fully integrated bench-top pattern generator instrument. The 12060 combines high-quality output with **programmable** data rate, amplitude, offset, crossing point, and patterns. The Model 12060 features high-performance DC coupled limiting amplifiers that result in programmable, accurate, fast risetime data signals. In addition, the 12060 provides many useful test features such as single bit error injection.

This generator provides leading performance and features at an economical price making it a compelling choice for high speed serial data testing.

# Ordering Information Model 12060 20Gb/s Programmable Pattern Generator

#### **Contact Information**

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# Specifications:

Description:	Value/Details	Notes
Data Outputs:		Differential/complimentary output.
Amplitude Single ended Differential	250mV to 2.0V 500mV to 4.0V	Positive and negative differential outputs are independently programmable.
Offset	-2.0V to +3.0V window	Programmable/adjustable
Termination Voltage	-2.0V to +3.3V	Programmable/adjustable
Crossing point adjust range	35% to 65% typ.	Programmable/adjustable
Risetime 20% to 80% 10% to 90%	17 ps typ. 25 ps typ.	Measured with 70GHz bandwidth sampling head directly attached to Model 12060 output connector [1]
Total Jitter	1.0 ps RMS typ.	Measured with 2 <sup>7</sup> -1 PRBS.
Output impedance	50 $\Omega$ single ended	100 Ω differential
Other Outputs:		
Trigger/Divided Clk	Swings from -600mV to 0V	User selectable as pattern trigger (one trigger at start of pattern) or clock/n (user selected n). DC coupled.
Clock signal Amplitude Jitter	400mVpp typ. <1 ps RMS typ.	AC coupled
Inputs:		
External clock input	400mVpp typ. 1Vpp max	AC coupled. Operates over range of 10GHz to 20GHz.
Pattern Generator:		
Data rate	1.25 Gb/s to 20 Gb/s	Data rate is programmable from front panel or computer control.
Frequency resolution	10kHz	
Built-in PRBS patterns	2 <sup>n</sup> -1	n = 7, 15, 23, 31
User defined pattern depth	4 Mbit	
Single bit error injection	Yes	
User Interface:		
Front panel touch screen GUI	Yes	Edit output characteristics, patterns, and instrument setup.
Computer programmable Interface	Yes USB	USB HID interface. Windows DLL provided for communicating with instrument.

Interface

USB for communicating with instrument.

[1] Measured risetime is the result of the combination of the risetime of the pattern generator, cables, attenuators, adapters, and the measurement device (e.g. a sampling oscilloscope).

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